

West Aval on West Box West Track

```

1E7 20AUG12 16:14:31.4 PCI Message slot number mismatch: 2
OE2 20AUG12 17:50:14.4 TK 2 RX: CODE( 5 M 1 6 ) (NCD ) VO(OFF)
OE2 20AUG12 17:50:17.1 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE2 20AUG12 19:03:51.8 TK 2 RX: CODE( 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 19:03:54.3 TK 2 RX: CODE( 7 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 19:04:37.2 TK 2 RX: CODE( 5 M 1 6 ) (NCD ) VO(OFF)
OE2 20AUG12 19:04:39.9 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE3 20AUG12 19:27:19.9 TK 2 TX: CODE( 7 1 ) ( ) VO(075)
OE2 20AUG12 19:42:01.7 TK 2 RX: CODE( M 1 ) (NCD ) VO(OFF)
OE2 20AUG12 19:54:00.3 TK 2 RX: CODE( M 1 ) ( ) VO(OFF)
OE2 20AUG12 19:54:07.9 TK 2 RX: CODE( ) ( ) VO(OFF)
OE3 20AUG12 19:54:08.6 TK 2 TX: CODE( 2 1 ) ( ) VO(075)
OE2 20AUG12 20:03:48.3 TK 2 RX: CODE( ) (NCD ) VO(OFF)
OE2 20AUG12 20:04:02.9 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE2 20AUG12 20:04:08.3 TK 2 RX: CODE( 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 20:04:11.0 TK 2 RX: CODE( 7 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 21:40:31.7 TK 2 RX: CODE( 5 M 1 6 ) (NCD ) VO(OFF)
OE2 20AUG12 21:40:34.3 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE3 20AUG12 22:19:06.5 TK 2 TX: CODE( 7 1 ) ( ) VO(075)
OE2 20AUG12 22:22:59.9 TK 2 RX: CODE( M 1 ) (NCD ) VO(OFF)
OE2 20AUG12 22:35:38.9 TK 2 RX: CODE( M 1 ) ( ) VO(OFF)
OE2 20AUG12 22:35:46.4 TK 2 RX: CODE( ) ( ) VO(OFF)
OE3 20AUG12 22:35:47.4 TK 2 TX: CODE( 2 1 ) ( ) VO(075)
OE2 20AUG12 22:44:39.6 TK 2 RX: CODE( ) (NCD ) VO(OFF)
OE2 20AUG12 22:44:54.1 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE2 20AUG12 22:44:59.4 TK 2 RX: CODE( 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 22:45:02.0 TK 2 RX: CODE( 7 5 M 1 ) (VCD ) VO(OFF)
OE2 20AUG12 23:17:31.8 TK 2 RX: CODE( 5 M 1 6 ) (NCD ) VO(OFF)
OE2 20AUG12 23:17:34.5 TK 2 RX: CODE( 5 M 1 ) (NCD ) VO(OFF)
OE3 20AUG12 23:30:48.4 TK 2 TX: CODE( 7 1 ) ( ) VO(075)
OE2 20AUG12 23:44:12.4 TK 2 RX: CODE( M 1 ) (NCD ) VO(OFF)
OE3 21AUG12 00:08:59.9 TK 2 TX: CODE( 2 1 ) ( ) VO(075)
1E7 21AUG12 05:57:16.0 PCI Message slot number mismatch: 2
OE2 21AUG12 16:28:38.6 TK 2 RX: CODE( M 1 ) ( ) VO(OFF)
OE2 21AUG12 16:28:46.1 TK 2 RX: CODE( ) ( ) VO(OFF)
1E7 21AUG12 17:43:14.2 PCI Message slot number mismatch: 2
1E7 21AUG12 18:05:53.8 PCI Message slot number mismatch: 2
OE2 22AUG12 01:15:24.8 TK 2 RX: CODE( ) (NCD ) VO(OFF)
OE2 22AUG12 01:15:38.9 TK 2 RX: CODE( M 1 ) (NCD ) VO(OFF)
OE3 22AUG12 01:42:40.5 TK 2 TX: CODE( 7 1 ) ( ) VO(075)
OE2 22AUG12 01:45:32.3 TK 2 RX: CODE( M 1 ) ( ) VO(OFF)
OE2 22AUG12 01:45:39.8 TK 2 RX: CODE( ) ( ) VO(OFF)
OE3 22AUG12 01:45:40.7 TK 2 TX: CODE( 2 1 ) ( ) VO(INV)
OE2 22AUG12 01:55:14.2 TK 2 RX: CODE( ) (NCD ) VO(OFF)
OE2 22AUG12 01:55:28.4 TK 2 RX: CODE( M 1 ) (NCD ) VO(OFF)
1E7 22AUG12 03:59:26.3 PCI Message slot number mismatch: 2
OE2 22AUG12 15:02:30.3 TK 2 RX: CODE( M 1 ) ( ) VO(OFF)
OE2 22AUG12 15:02:40.3 TK 2 RX: CODE( ) ( ) VO(OFF)
OE2 22AUG12 15:03:31.5 TK 2 RX: CODE( ) (NCD ) VO(OFF)
OE2 22AUG12 15:03:45.2 TK 2 RX: CODE( 1 ) (NCD ) VO(OFF)
END OF BUFFER

```