

East Davis East Track Module 08-29012

OE2	20AUG12	19:45:18.9	TK 2 RX: CODE( M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	19:45:21.6	TK 2 RX: CODE( 2 M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	19:51:35.2	TK 2 RX: CODE( 7 M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	19:53:20.0	TK 2 RX: CODE( 7 M 1 )	( )	VO(OFF)
OE2	20AUG12	19:53:22.5	TK 2 RX: CODE( M 1 )	( )	VO(OFF)
OE2	20AUG12	19:53:30.0	TK 2 RX: CODE( )	( )	VO(OFF)
OE2	20AUG12	19:56:41.6	TK 2 RX: CODE( )	(NCD )	VO(OFF)
OE2	20AUG12	19:56:45.4	TK 2 RX: CODE( )	(VCD )	VO(OFF)
OE2	20AUG12	19:56:56.1	TK 2 RX: CODE( M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	19:56:58.8	TK 2 RX: CODE( 2 M 1 )	(VCD )	VO(OFF)
OE3	20AUG12	19:57:00.4	TK 2 TX: CODE( 2 1 )	( )	VO(075)
OE2	20AUG12	19:57:04.2	TK 2 RX: CODE( 7 M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	19:58:22.1	TK 2 RX: CODE( 7 5 M 1 )	(VCD )	VO(OFF)
1E7	20AUG12	20:47:04.7	PCI Message slot number mismatch: 2		
OE3	20AUG12	21:40:28.5	TK 2 TX: CODE( 1 6 )	( )	VO(INV)
OE2	20AUG12	22:16:53.6	TK 2 RX: CODE( 7 5 M 1 )	( )	VO(OFF)
OE2	20AUG12	22:16:56.2	TK 2 RX: CODE( 5 M 1 )	( )	VO(OFF)
OE2	20AUG12	22:17:03.7	TK 2 RX: CODE( )	( )	VO(OFF)
OE2	20AUG12	22:25:04.1	TK 2 RX: CODE( )	(NCD )	VO(OFF)
OE2	20AUG12	22:25:05.1	TK 2 RX: CODE( )	(VCD )	VO(OFF)
OE2	20AUG12	22:25:18.4	TK 2 RX: CODE( M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	22:25:21.1	TK 2 RX: CODE( 2 M 1 )	(VCD )	VO(OFF)
OE3	20AUG12	22:25:21.9	TK 2 TX: CODE( 2 1 )	( )	VO(INV)
OE2	20AUG12	22:32:04.8	TK 2 RX: CODE( 7 M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	22:39:06.9	TK 2 RX: CODE( 7 5 M 1 )	(VCD )	VO(OFF)
OE3	20AUG12	23:17:29.0	TK 2 TX: CODE( 1 6 )	( )	VO(075)
OE2	20AUG12	23:38:34.3	TK 2 RX: CODE( 7 5 M 1 )	( )	VO(OFF)
OE2	20AUG12	23:38:36.8	TK 2 RX: CODE( 5 M 1 )	( )	VO(OFF)
OE2	20AUG12	23:38:41.8	TK 2 RX: CODE( )	( )	VO(OFF)
OE2	20AUG12	23:45:40.8	TK 2 RX: CODE( )	(NCD )	VO(OFF)
OE2	20AUG12	23:45:41.9	TK 2 RX: CODE( )	(VCD )	VO(OFF)
OE2	20AUG12	23:45:55.4	TK 2 RX: CODE( M 1 )	(VCD )	VO(OFF)
OE2	20AUG12	23:45:58.0	TK 2 RX: CODE( 2 M 1 )	(VCD )	VO(OFF)
OE3	20AUG12	23:45:59.0	TK 2 TX: CODE( 2 1 )	( )	VO(075)
OE2	20AUG12	23:52:22.3	TK 2 RX: CODE( 7 M 1 )	(VCD )	VO(OFF)
1E7	21AUG12	03:59:21.3	PCI Message slot number mismatch: 2		
1E7	21AUG12	06:59:22.4	PCI Message slot number mismatch: 2		
1E7	21AUG12	07:33:05.3	PCI Message slot number mismatch: 2		
OE3	21AUG12	10:23:25.1	TK 2 TX: CODE( 1 6 )	( )	VO(INV)
OE2	21AUG12	10:31:20.1	TK 2 RX: CODE( 7 M 1 )	( )	VO(OFF)
OE2	21AUG12	10:31:22.6	TK 2 RX: CODE( M 1 )	( )	VO(OFF)
OE2	21AUG12	10:31:27.6	TK 2 RX: CODE( )	( )	VO(OFF)
OE2	21AUG12	10:36:52.9	TK 2 RX: CODE( )	(NCD )	VO(OFF)
OE2	21AUG12	10:36:54.1	TK 2 RX: CODE( )	(VCD )	VO(OFF)
OE2	21AUG12	10:37:07.3	TK 2 RX: CODE( M 1 )	(VCD )	VO(OFF)
OE2	21AUG12	10:37:09.9	TK 2 RX: CODE( 2 M 1 )	(VCD )	VO(OFF)
OE3	21AUG12	10:37:10.7	TK 2 TX: CODE( 2 1 )	( )	VO(075)
OE2	21AUG12	10:56:48.1	TK 2 RX: CODE( 7 M 1 )	(VCD )	VO(OFF)
1E7	21AUG12	11:37:01.4	PCI Message slot number mismatch: 2		
OE3	21AUG12	12:19:15.3	TK 2 TX: CODE( 7 1 )	( )	VO(INV)
1E7	21AUG12	15:00:00.4	PCI Message slot number mismatch: 2		